	Application No.	Applicant(s)
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Notice of Allowability	10/616,223	SHAKUDA ET AL.
	Examiner	Art Unit
	Fazli Erdem	2826
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>Amendment filed on 7</u>	7/19/2004.	,
2. ☑ The allowed claim(s) is/are <u>1-14</u> .		NATHAN J. FLYNN
3. The drawings filed on 10 July 2003 are accepted by the Ex	aminer. SUPE	ERVISORY PATENT EXAMINER ECHNOLOGY CENTER 1895
 4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some* c) None of the: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No		
7. DEPOSIT OF and/or INFORMATION about the depose attached Examiner's comment regarding REQUIREMENT F	FOR THE DEPOSIT OF BIOLO	GICAL MATERIAL.
Attachment(s)		
1. Notice of References Cited (PTO-892)		nal Patent Application (PTO-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. ☐ Interview Summ Paper No./Mail	nary (₽ ГО-413), ≀ Date
3. Information Disclosure Statements (PTO-1449 or PTO/SB/06 Paper No./Mail Date	8), 7. Examiner's Amo	endment/Comment
4. Examiner's Comment Regarding Requirement for Deposit		ement of Reasons for Allowance
of Biological Material	9. Other	

Art Unit: 2826

DETAILED ACTION

Allowable Subject Matter

- 1. Claims 1-14 allowed.
- 2. The following is an examiner's statement of reasons for allowance: Prior art failed to establish a semiconductor light emitting device comprising a semiconductor substrate, a semiconductor laminating section with light emitting layer forming section with n and p type layers, a current a translucent current diffusing electrode, a bonding electrode, an electrode formed on the rear surface of the semiconductor substrate where the semiconductor laminating section and the current diffusing electrode are separated into a plurality of light emitting unit sections, an electrode pad section, and connecting section for connecting between the electrode pad portion and the light emitting unit sections where the bonding electrode is disposed on the electrode pad section and electrode pad section is formed to make light emitting layer section to be non-luminous.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Fazli Erdem whose telephone number is (571) 272-1914. The examiner can normally be reached on M - F 8:00 - 5:00.

Art Unit: 2826

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nathan Flynn can be reached on (571) 272-1915. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

FE September 28, 2004